

36. (New) The sample-and-hold amplifier circuit of claim 17, wherein the resistor is realized by an ON resistor of a transistor.

37. (New) The sample-and-hold amplification circuit of claim 19, wherein the resistor is realized by an ON resistor of a transistor.

38. (New) The sample-and-hold amplification circuit of claim 20, wherein the resistor is realized by an ON resistor of a transistor.

39. (New) The sample-and-hold amplification circuit of claim 35, wherein the resistor is realized by an ON resistor of a transistor.

REMARKS

This application is a divisional of Serial No. 09/932,025. Claims 1-15 have been canceled. New claims 16-39 have been added. Thus, claims 16-39 are now pending.

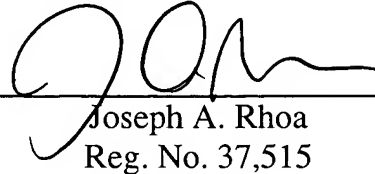
It is respectfully submitted that all claims are in condition for allowance. If any minor matter remains to be resolved, the Examiner is invited to telephone the undersigned with regard to the same.

FUJIMOTO
Serial No. To be Assigned
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Respectfully submitted,

NIXON & VANDERHYE P.C.

By: _____


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